H.K.E. SOCIETY'S
POOJYA DODDAPPA APPA COLLEGE OF ENGINEERING, KALABURAGI.
(AUTONOMOUS INSTITUTION)

FINAL TIME TABLE OF B.E. V SEM. AUTONOMOUS THEORY (SUMMER TERM) EXAMINATIONS FOR NOVEMBER/DECEMBER - 2024.

REVISED ON 23.11.2024

Date & Day TIME :	ELECTRONICS & COMM. ENGINEERING 9.30 A.M. to 12.30 P.M. SUB.CODE & SUBJECT	ELECTRICAL & ELECTRONICS 9.30 A.M. to 12.30 P.M. SUB.CODE & SUBJECT	COMPUTER SC. & ENGINEERING 9.30 A.M. to 12.30 P.M. SUB.CODE & SUBJECT	ELECTRONICS & INSTRUMENTATION ENGINEERING 9.30 A.M. to 12.30 P.M. SUB.CODE & SUBJECT	INFORMATION SC. & ENGINEERING 9.30 A.M. to 12.30 P.M. SUB.CODE & SUBJECT
22.11.2024 FRIDAY	18/19EC52 DIGITAL SIGNAL PROCESSING	19EE52 POWER ELECTRONICS	19CS52 FAFL	19EI52 PROCESS CONTROL	19IS52/16IS52 DATA COMMUNICATION
26.11.2024 TUESDAY		19EE51 ELECTRICAL MACHINE DESIGN	19CS53 DATA BASE MANAGEMENT SYSTEM	19EI53 COMMUNICATION SYSTEMS	18/19IS53 DATABASE MANAGEMENT SYSTEM
29.11.2024 FRIDAY	19EC54 DIGITAL COMMUNICATION	19EE54 POWER SYSTEM ANALYSIS & STABILITY	19CS54 SOFTWARE ENGG., AND TESTING	19EI54 BIOMEDICAL INSTRUMENTATION	19IS54 JAVA PROGRAMMING
03.12.2024 TUESDAY	19EC55 EMBEDDED MICROCONTROLLER	19EE55 MICROCONTROLLERS	19CS55/18CS55 ARTIFICIAL INTELLIGENCE		19IS55 SOFTWARE ENGINEERING
04.12.2024 WEDNESDAY		19EE55 MICROCONTROLLERS			
					19IS56 COMPILER DESIGN
06.12.2024 FRIDAY	19HU01 RECRUITMENT PROCESS TRAINING - I	19HU01 RECRUITMENT PROCESS TRAINING - I	19HU01 RECRUITMENT PROCESS TRAINING - I	19HU01 RECRUITMENT PROCESS TRAINING - I	19HU01 RECRUITMENT PROCESS TRAINING - I

AFTER NOON SESSION 2.00 P.M. TO 5.00 P.M.

Date & Day	ELECTRONICS & COMM.	ELECTRICAL & ELECTRONICS	COMPUTER SC. & ENGINEERING	ELECTRONICS & INSTRUMENTATION	INFORMATION SC. & ENGINEERING
19.11.2024 TUESDAY	18/19EC51 LINEAR CONTROL SYSTEMS	19EE41/21EE51 SIGNALS AND SYSTEMS	19CS51 PYTHON. PROGRAMMING	19EI51 ARM PROCESSOR	18/19IS51 OPERATING SYSTEM
26.11.2024 TUESDAY	19EC53 INFORMATION THEORY & CODING				

CONTROLLER OF EXAMINATIONS
PDA COLLEGE OF ENGG., KALABURAGI.

PRINCIPAL
PDA COLLEGE OF ENGG., KALABURAGI

POOJYA DODDAPPA APPA COLLEGE OF ENGINEERING, GULBARGA.

(AUTONOMOUS INSTITUTION)

FINAL TIME TABLE OF B.E. VI SEM. (SUMMER TERM) AUTONOMOUS THEORY EXAMINATIONS FOR NOVEMBER/DECEMBER 2024.

REVISED ON 23.11.2024

Date & Day	ELECTRICAL & ELECTRONICS	ELECTRONICS & COMM. ENGINEERING	COMPUTER SC. & ENGINEERING	ELECTRONICS & INSTRUMENTATION ENGINEERING	INFORMATION SC. & ENGINEERING
TIME :	2.00 P.M. to 5.00 P.M.	2.00 P.M. to 5.00 P.M.	2.00 P.M. to 5.00 P.M.	2.00 P.M. to 5.00 P.M.	2.00 P.M. to 5.00 P.M.
	SUB.CODE & SUBJECT	SUB.CODE & SUBJECT	SUB.CODE & SUBJECT	SUB.CODE & SUBJECT	SUB.CODE & SUBJECT
22.11.2024 FRIDAY		**19EC62** ANTENNA AND WAVE PROPAGATION			**19IS62** ARTIFICIAL INTELLIGENCE
26.11.2024 TUESDAY			**19CS63** MACHINE LEARNING	**19EI631** INDUSTRIAL DATA COMMUNICATION AND NETWORKS	**19IS631** COMPUTER NETWORKS
29.11.2024 FRIDAY	**19EE644** ESSENTIAL OF INFORMATION TECHNOLOGY	**19EC64** DATA STRUCTURES USING C++	**19CS641** INTERNET OF THINGS		**19IS643** DATA MINING AND WAREHOUSING
03.12.2024 TUESDAY			**19CS652/19CS651** WEB SERVICES / BUILDING ENTERPRISE APPLICATIONS		**18/19IS651** CRYPTOGRAPHY AND BLOCK CHAIN
04.12.2024 WEDNESDAY			**19CS62/16CS63** COMPUTER NETWORKS		
06.12.2024 FRIDAY	**19EE6OE4** ELECTRIC VEHICLES	**19EC561** INTERNET OF THINGS		**19EI6OE** TRANSDUCERS AND PROCESS INSTRUMENTATION	**19IS6OE** SOFTWARE TESTING
	19HU02 RECRUITMENT PROCESS TRAINING - II	**19HU02** RECRUITMENT PROCESS TRAINING - II	**19HU02** RECRUITMENT PROCESS TRAINING - II	**19HU02** RECRUITMENT PROCESS TRAINING - II	**19HU02** RECRUITMENT PROCESS TRAINING - II

MORNING SESSION 9.30 A.M. TO 12.30 P.M.

	ELECTRICAL & ELECTRONICS	ELECTRONICS & COMM. ENGINEERING	COMPUTER SC. & ENGINEERING	ELECTRONICS & INSTRUMENTATION ENGINEERING	INFORMATION SC. & ENGINEERING
19.11.2024 TUESDAY	**19HU61** ENTERPRENCURSHIP MANAGEMENT & FINANCE	**19HU61** ENTERPRENCURSHIP MANAGEMENT & FINANCE	**19HU61** ENTERPRENCURSHIP MANAGEMENT & FINANCE	**19HU61** ENTERPRENCURSHIP MANAGEMENT & FINANCE	**19HU61** ENTERPRENCURSHIP MANAGEMENT & FINANCE
26.11.2024 TUESDAY		**19EC63** DIGITAL DESIGN USING VERILOG HDL			

CONTROLLER OF EXAMINATIONS
PDA COLLEGE OF ENGG., KALABURGI.

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