

**(AUTONOMOUS INSTITUTION)**


DRAFT TIME TABLE OF B.E. V SEM. (RE-REGISTERED) AUTONOMOUS THEORY EXAMINATIONS FOR JANUARY/FEBRUARY - 2025.

| Date & Day                  | ELECTRONICS & COMM. ENGINEERING | ELECTRICAL & ELECTRONICS          | COMPUTER SC. & ENGINEERING          | ELECTRONICS & INSTRUMENTATION ENGINEERING | INFORMATION SC. & ENGINEERING      |
|-----------------------------|---------------------------------|-----------------------------------|-------------------------------------|---|------------------------------------|
| TIME :                      | 9.30 A.M. to 12.30 P.M.         | 9.30 A.M. to 12.30 P.M.           | 9.30 A.M. to 12.30 P.M.             | 9.30 A.M. to 12.30 P.M.                   | 9.30 A.M. to 12.30 P.M.            |
|                             | SUB.CODE & SUBJECT              | SUB.CODE & SUBJECT                | SUB.CODE & SUBJECT                  | SUB.CODE & SUBJECT                        | SUB.CODE & SUBJECT                 |
|                             | <b>19EC51</b>                   | <b>19EE52</b>                     | <b>19CS51</b>                       |   | <b>18/19IS55</b>                   |
| <b>28.01.2025 TUESDAY</b>   | LINEAR CONTROL SYSTEMS          | POWER ELECTRONICS                 | PYTHON. PROGRAMMING                 |   | SOFTWARE ENGINEERING               |
|                             | <b>19EC52</b>                   | <b>19EE55</b>                     | <b>19CS52</b>                       | <b>19EI51</b>                             | <b>19IS53</b>                      |
| <b>30.01.2025 THURSDAY</b>  | DIGITAL SIGNAL PROCESSING       | MICROCONTROLLERS                  | FINITE AUTOMATA AND FORMAL LANGUAGE | ARM PROCESSOR                             | DATABASE MANAGEMENT SYSTEM         |
|                             | <b>19EC53</b>                   | <b>19EE54</b>                     | <b>19CS53</b>                       | <b>19EI52</b>                             | <b>18/19IS51</b>                   |
| <b>03.02.2025 MONDAY</b>    | INFORMATION THEORY & CODING     | POWER SYSTEM ANALYSIS & STABILITY | DATA BASE MANAGEMENT SYSTEM         | PROCESS CONTROL                           | OPERATING SYSTEM                   |
|                             | <b>19EC54</b>                   | <b>19EE53</b>                     | <b>19CS54</b>                       | <b>19EI53</b>                             | <b>19IS54/19IS56</b>               |
| <b>05.02.2025 WEDNESDAY</b> | DIGITAL COMMUNICATION           | SIGNALS & SYSTEMS                 | SOFTWARE ENGG., AND TESTING         | COMMUNICATION SYSTEMS                     | JAVA PROGRAMMING / COMPLIER DESIGN |
|                             | <b>19EC55</b>                   | <b>19EE51</b>                     | <b>19CS55/18CS55</b>                | <b>19EI54</b>                             | <b>19IS52/16IS52</b>               |
| <b>07.02.2025 FRIDAY</b>    | EMBEDDED MICROCONTROLLER        | ELECTRICAL MACHINE DESIGN         | ARTIFICIAL INTELLIGENCE             | BIOMEDICAL INSTRUMENTATION                | DATA COMMUNICATION                 |

**AFTER NOON SESSION 2.00 P.M. TO 5.00 P.M.**

|                          |                                  |                                  |                                  |                                  |                                  |
|--------------------------|----------------------------------|----------------------------------|----------------------------------|----------------------------------|----------------------------------|
|                          | <b>19HU01</b>                    | <b>19HU01</b>                    | <b>19HU01</b>                    | <b>19HU01</b>                    | <b>19HU01</b>                    |
| <b>07.02.2025 FRIDAY</b> | RECRUITMENT PROCESS TRAINING - I | RECRUITMENT PROCESS TRAINING - I | RECRUITMENT PROCESS TRAINING - I | RECRUITMENT PROCESS TRAINING - I | RECRUITMENT PROCESS TRAINING - I |

  
CONTROLLER OF EXAMINATIONS  
PDA COLLEGE OF ENGG., KALABURAGI.

  
PRINCIPAL,  
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