

**H.K.E. SOCIETY'S**  
**POOJYA DODDAPPA APPA COLLEGE OF ENGINEERING, KALABURAGI**  
**(AUTONOMOUS INSTITUTION AFFILIATED TO VTU, BELGAUM)**  
**DETAIL TIME TABLE FOR B.E. V SEM. AUTONOMOUS THEORY EXAMINATIONS FOR FEB./ MARCH 2023.**

**REVISED ON 03.02.2023**



Date & Day	ELECTRONICS & COMM. ENGINEERING	ELECTRICAL & ELECTRONICS	COMPUTER SC. & ENGINEERING	ELECTRONICS & INSTRUMENTATION ENGINEERING	INFORMATION SC. & ENGINEERING
TIME :	9.30 A.M. TO 12.30 P.M.	9.30 A.M. TO 12.30 P.M.	9.30 A.M. TO 12.30 P.M.	9.30 A.M. TO 12.30 P.M.	9.30 A.M. TO 12.30 P.M.
	SUB.CODE & SUBJECT	SUB.CODE & SUBJECT	SUB.CODE & SUBJECT	SUB.CODE & SUBJECT	SUB.CODE & SUBJECT
	<b>19EC51</b>	<b>19EE51</b>	<b>19CS51/18CS51</b>	<b>19EI51</b>	<b>19IS51</b>
<b>14.02.2023 TUESDAY</b>	LINEAR CONTROL SYSTEMS	ELECTRICAL MACHINE DESIGN	PYTHON. PROGRAMMING / DATA SCIENCE	ARM PROCESSOR	OPERATING SYSTEM
	<b>19EC52</b>	<b>19EE52</b>	<b>19CS52 /18CS52</b>	<b>19EI52</b>	<b>19IS52/16IS52</b>
<b>16.02.2023 THURSDAY</b>	DIGITAL SIGNAL PROCESSING	POWER ELECTRONICS	FAFL / DATA COMMUNICATION	PROCESS CONTROL	DATA COMMUNICATION
	<b>19EC53/16EC53</b>	<b>19EE53/18EE53</b>	<b>19CS53</b>	<b>19EI53</b>	<b>19IS53</b>
<b>20.02.2023 MONDAY</b>	INFORMATION THEORY & CODING / ANALOG COMMUNICATION	UTILIZATION OF ELECTRICAL POWER / SIGNALS & SYSTEMS	DATA BASE MANAGEMENT SYSTEM	COMMUNICATION SYSTEMS	DATABASE MANAGEMENT SYSTEM
	<b>19EC54</b>	<b>19EE54</b>	<b>19CS54</b>	<b>19EI54</b>	<b>19IS54</b>
<b>22.02.2023 WEDNESDAY</b>	DIGITAL COMMUNICATION	POWER SYSTEM ANALYSIS & STABILITY	SOFTWARE ENGG., AND TESTING	BIOMEDICAL INSTRUMENTATION	JAVA PROGRAMMING
	<b>19EC55/16EC55</b>	<b>19EE55</b>	<b>19CS55/18CS55</b>	<b>19EI55</b>	<b>19IS55</b>
<b>24.02.2023 FRIDAY</b>	EMBEDDED MICROCONTROLLER / TRANSMISSION LINES AND WAVEGUIDES	MICROCONTROLLERS	ARTIFICIAL INTELLIGENCE	SCIENTIFIC AND ANALYTICAL INSTRUMENTATION	SOFTWARE ENGINEERING
	<b>19HU01</b>	<b>19HU01</b>	<b>19HU01</b>	<b>19HU01</b>	<b>19HU01</b>
<b>27.02.2023 MONDAY</b>	RECRUITMENT PROCESS TRAINING - I	RECRUITMENT PROCESS TRAINING - I	RECRUITMENT PROCESS TRAINING - I	RECRUITMENT PROCESS TRAINING - I	RECRUITMENT PROCESS TRAINING - I
					<b>19IS56</b>
<b>01.03.2023 WEDNESDAY</b>					COMPILER DESIGN
					<b>18IS54</b>
<b>02.03.2023 THURSDAY</b>					WEB TECHNOLOGY AND J2EE

*3/2/2023*  
**Prof. Ravindra M. Lathe**  
 CONTROLLER OF EXAMINATIONS  
 PDA COLLEGE OF ENGG., KALABURAGI.  
**PDA College of Engineering**  
**KALABURAGI**

*Ramesh*  
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